

Adarsh Singh Niranjana

Mater of Technology - Signal Processing (ECED)
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CAREER OBJECTIVE

To obtain a position that challenges me and provides me the opportunity to reach my full potential professionally and personally utilizing my abilities and my experience in your organization that is progressive intellectually and technically and one in which practices collaborative leadership, integrity, and honesty.

ACADEMIA

YEAR	DEGREE	INSTITUTE	Aggregate
2019-21	M.Tech (Signal Processing)	Motilal Nehru National Institute of Technology, Allahabad, U.P.	9.4/10 (Till 3 rd Sem)
2015-19	B.Tech (ECE)	Indian Institute of Information Technology, Design & Manufacturing, Jabalpur, M.P	7.9 /10
2014	XII (UP Board)	SVMIC Hamirpur (U.P)	94.4%
2012	X (UP Board)	SVMIC Hamirpur (U.P.)	88.17%

TECHNICAL SKILLS

- **Programming Languages:** VERILOG, Basics of C
- **Computation Platform:** MATLAB.
- **Design Platforms & Simulators:** ISE Xilinx
- **Assembly Language:** Microprocessor 8085

PROFESSIONAL TRAINING

VLSI Design.....Central Tool Room & Training Centre, Bhubaneswar
The course comprised the following subjects: (01/06/17 - 30/6/17)

- ❖ Basics of VLSI
- ❖ VERILOG programming with XILINX
- ❖ Basics of Microcontroller.

PROJECTS

- **Brain Tumor Detection using Image Processing Techniques**

Course: M. Tech Thesis

— Presented an efficient method which includes k-means clustering using watershed algorithm and optimized c-means using genetic algorithm for brain tumor detection.

Mentor: Dr. V.K. Rao, ECE Department, MNNIT Allahabad

- **Face Recognition using MATLAB**

Course: *Digital Signal Processing*

- Develop a very efficient algorithm, in terms of low computational complexity, with the maximum number of true face detection and the minimum number of false alarms.

Mentor: Prof. V.K. Srivastava & Dr. V.K. Rao, ECE Department, MNNIT Allahabad

- **Merging of Thermionic Emission & Tunneling Phenomena: Proposal and Optimization**

Course: *Project Based Internship (VLSI)*

- Improvement in ON state current of conventional physically doped TFET (C-PD-TFET)
- Enhancement in DC characteristics and RF parameters of C-PD-TFET.

Mentor: Late Dr. Dheeraj Sharma, Assistant Professor (ECE), IIITDM Jabalpur

- **Heart Attack Alert & Automatic Vehicle Speed Control Device**

Course: *Design and Fabrication Project*

- 24/7 heart monitoring so if the heart stops or slows down; friends, family, and emergency services are alerted.
- Speed controlling of the vehicle if the driver is in fatigue condition, excitement, fear or has consumed alcohol; based on fluctuation in his heart beats.

Mentor: Prof. Puneet Tandon, Professor (Mechanical & Design), IIITDM Jabalpur

- **Automatic Smart Car Parking System CTTC, Bhubaneswar**

Course: *VLSI Design*

- A project on 'Automatic Smart Car Parking System' was successfully completed wherein Verilog codes were developed to achieve the goal.

- **MOSFET OPERATIONAL AMPLIFIER**

Course: *Linear Integrated Circuits*

- The aim of this project is to show operational amplifier working using MOSFET instead of BJT.
- The whole internal structure of Op-amp is developed by MOSFET in MULTISIM simulator.

Mentor: Dr. Irshad Ahmad Ansari, Assistant Professor (ECE), IIITDM Jabalpur

- **Self-Projects**

- Line Follower Robot.
- Embedded C Mini Projects - LED Matrix animation, Circuit simulation, Seven-segment and LCD Display, Calculator, Thermometer using ATMEGA-8.
- Design of Flash ADC and Binary Weighted DAC on NI ELVIS II.
- Simulation of n-MOSFET input-output characteristics using PSpice Schematic (Cadence).

REWARDS AND ACHIEVEMENTS

- ❖ **Award of Excellence**

Achieved 15th rank in UP board Intermediate exam, 2014 and awarded by Honorable Former Chief Minister, Mr. Akhilesh Yadav (U.P.).

- ❖ **Certificate of Merit**

Received Appreciation Certificate from District Magistrate, Hamirpur (UP) for one of the top 10 Students in the whole district in High-school UP Board exam, 2012.

- ❖ **State runner-up in Mathematical Model of Conic Section**

1st rank in Chitrakoot Division, B.N.S.D. Shiksha Niketan, Kanpur;
2nd rank at the state level, SVMIC, Sultanpur (U.P.).

❖ **All India Talent Search Examination, NTC**

Excellence certificate for securing more than 60 %.

❖ **Medhavi Chatra Yogyata Pariksha, Vidya Bharti**

Got 9th rank in all over India's Vidya Bharti Schools.

CO-CURRICULAR ACTIVITIES

- **Competition Won:**
 - ❖ 3rd position in LED Matrix Gaming, Inter IIIT Techno-Cultural Fest, 2017, IIITDM Jabalpur.
- **Responsibilities held:**
 - ❖ Assistant Coordinator, Counseling Services, PDPM IIITDMJ.
 - ❖ Volunteer, DEW 2016, PDPM IIITDMJ.
 - ❖ Event Coordinator: Abhikalpan 2016 & Tarang 2015, PDPM IIITDMJ.
 - ❖ Volunteer, Madhya Pradesh State AIDS Control Society's Blood Donation Camp, 2016, JAGRATI, PDPM IIITDMJ.
 - ❖ Participator, Campus to Corporate Webinar, 6th-9th June, 2020, IIT BHU

PERSONAL DETAILS

- **DOB:** 05-Jan-1998
- **Languages Known:** Hindi, English, Sanskrit
- **Address:** T-II/7, District Hospital Colony, Hamirpur (U.P.) - 210301
- **Hobbies:** Online gaming, Traveling, Web surfing

DECLARATION

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibilities for the correctness of above-mentioned particulars.