


**Office of Dean (AA&IR)**  
**IIT Kharagpur**

Dt: 15<sup>th</sup> September, 2009

Enclosed please find a copy of the MOU between Synopsys Inc., USA and Indian Institute of Technology Kharagpur for setting up the **“Synopsys CAD Laboratory at IIT Kharagpur”** duly signed by the Competent Authority of IIT Kharagpur & Synopsys Inc., USA.

Registrar may kindly take necessary action in this regard.

Encl: As stated

  
(Amit Patra)  
Dean (AA&IR)

REGISTRAR

## **Memorandum of Understanding**

between

**Synopsys Inc., USA**

and

**Indian Institute of Technology Kharagpur, India**

for setting-up the

### **“Synopsys CAD Laboratory at IIT Kharagpur”**

SYNOPSYS and the Indian Institute of Technology Kharagpur (IITKGP) have a continuing collaboration in VLSI CAD for over a decade. The collaboration has been successful and enriching. SYNOPSYS has also supported IITKGP with research projects, student internships and technology guidance. IITKGP has produced successful results in these projects, some of which have been very useful to SYNOPSYS. *Graduates from IITKGP have also joined SYNOPSYS as employees.*

SYNOPSYS has been a principal supporter and founder partner, along with the IIT Foundation and five other industries in the development of the Advanced VLSI Laboratory (AVLSI Lab) at IITKGP. Over the years the AVLSI Lab has been an example of how industry can support and bring true relevance to education and research in an academic institution. Over the years the AVLSI Lab has grown into a large umbrella of research activities with the support of various world-class partners including SYNOPSYS. Important activities among them include Chip Design, CAD, Verification and Test covering digital, analog and mixed signal designs through RF to multicore-processor architectures. Other areas ranging from electronic materials to target application domains like medical instrumentation and wireless communications are coming into the ambit of the overall AVLSI Lab umbrella. As another part of this activity an AVLSI Consortium with several major companies as partners has been formed to support student research in these areas. SYNOPSYS is an early member and key supporter of the AVLSI Consortium.

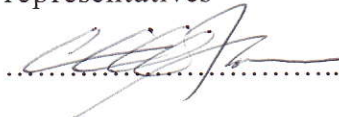
Hence, it is appropriate at this point to recognize CAD of VLSI as a distinct and noticeable part of the AVLSI Lab. SYNOPSYS recognizes the importance of supporting CAD tool development and research at IITKGP to help develop state-of-the-art talent and intellectual property. Through an anticipated definitive agreement between the parties, it is therefore proposed to set up the Synopsys CAD Laboratory at IIT KGP.



The parties desire to enter into a definitive agreement consistent with this MOU. This MOU is not intended to be binding but is intended to provide the basis for a future definitive agreement. Keeping in view the above, the following sets forth the mutual understanding and intentions of the parties:

1. SYNOPSYS shall grant hardware and licenses of its tools as per SYNOPSYS standard terms and conditions. In addition, SYNOPSYS will grant funding to support the operational cost of various research projects.
2. IITKGP shall designate one or two faculty members who shall be the coordinators for the Synopsys CAD Laboratory at IITKGP. Notwithstanding the SYNOPSYS reference in the name of Laboratory (a) IITKGP retains full rights and responsibility for the facility including without limitation the funding and operations thereof; and (b) the facility shall not be used in any manner that might imply any non-SYNOPSYS materials, including but not limited to goods, services, designs or publications, are sponsored, endorsed, licensed by or affiliated with SYNOPSYS.
3. SYNOPSYS shall continue its membership of the AVLSI Consortium and contribute to helping in guiding research. Through the AVLSI consortium, SYNOPSYS shall have a non-exclusive license of all the research results that are funded through the AVLSI Consortium as per the agreed terms.
4. SYNOPSYS shall help train interns of IITKGP. IITKGP shall encourage applicable candidates from SYNOPSYS to pursue masters and doctoral research from IITKGP. SYNOPSYS shall consider funding Outstanding Young Faculty, Student Fellowships and Chair Professor Fellowships at IITKGP. IITKGP shall encourage eligible experts of SYNOPSYS to participate in research through joint supervision and teaching through Adjunct Faculty positions.

This non-binding MOU is signed at IIT Kharagpur 721302, India on the 8<sup>th</sup> of September 2009 by SYNOPSYS and IITKGP through their authorized representatives

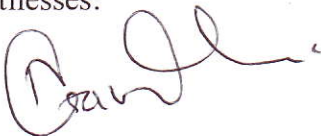


Synopsys



Indian Institute of Technology  
Kharagpur

Witnesses:

1. 

2.   
8/9/09